## Amendments to the Specification:

Please replace the paragraph beginning on page 11, line 1, and ending on page 11, line 3, with the following rewritten paragraph:

-- Fig. 8 and 8A is are schematic block diagram diagrams showing the construction of a signal processing apparatus according to a third embodiment; --.

Please replace the paragraph beginning on page 31, line 24, and ending on page 31, line 26, with the following rewritten paragraph:

-- Fig. Figs. 8 and 8A is are schematic block diagram diagrams showing a signal processing apparatus according to a third embodiment of the present invention. --.

Following the paragraph ending on page 38, line 17, add the following paragraph:

-- In the signal processing apparatus according to the third embodiment of the present invention of FIG. 8A, the CCD 1 has a pure-color type filter as described on page 12, lines 1-12, hereinabove, so that RGB signals are contained in its output. This avoids the need for a matrix circuit which is used in Fig. 8. Also, as in Fig. 8, the color suppression 21A operates on these signals as output from the A/D conversion circuit 2 and supplies the compressed signals to the color interpolation circuit 3 which performs an interpolation process. The color suppression circuit 21A is similar to the color suppression circuit 21 of Fig. 8 and the color interpolation circuit 3 is similar to the color interpolation circuit of Fig 8, but each is adapted to operate on RGB signals. Likewise, the A/D conversion circuit 2, the gamma correction circuit 6, the color-difference matrix circuit 7, the color carrier elimination circuit 8, the luminance color-difference correction circuit 9, the aperture correction circuit 10, the gamma correction circuit 11 and the correcting color-difference matrix 12 are also like the corresponding components of the third embodiment of the invention of Fig. 8. --.